

ATE for Array Interface Board

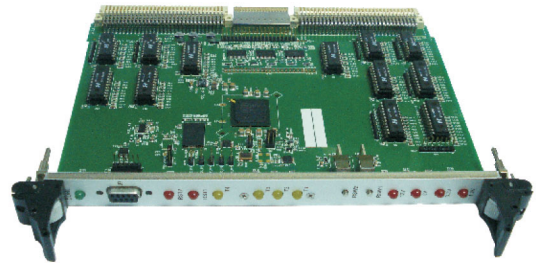
PRODUCT DESCRIPTION

ATE for Array Interface Board is a test jig board which will check the parameters of output signals from Array Interface Board. The ATE is designed to support automatic testing of Array interface boards and generate log report.

The Array Interface Board is a 6U board in VME form factor which has onboard FPGA. The board is used in interfacing with the antenna array in radar systems. The board is used for implementation of complex signal processing algorithms on FPGA.

KEY FEATURES

- Xilinx Kintex-7 325T FPGA
- 87 lines of 12V CMOS input interfaces
- Interface test for
 - Ethernet
 - UART
 - DDR2
- Onboard UART-USB converter
- RTL for validation of Array Interface Board
- Automatic scripts for diagnosing the board
- Electrical and data testing at interface
- Health monitor to detect onboard voltages of the ATE
- Onboard voltage regulators for generating DC
- Voltages from VME power supply



SPECIFICATIONS

FPGAs / Processor

- Xilinx Kintex-7 325T FPGA

Interfaces

- 10/100 Ethernet
- JTAG
- Onboard UART-USB converter
- 87 lines of 12V CMOS input interfaces

Software / IP

- RTL for validation of Array Interface Board
- Automatic scripts for diagnosing the board

Additional Information

- 1 GB NOR Flash
- 2 GB DDR2 RAM
- 200 MHz oscillator for FPGA and DDR2

MECHANICAL

- 6U VME air cooled board
- The board weighs 450 grams

POWER CONSUMPTION

- Power supply 12V, 5V, 3.3V available from standard VME backplane
- The unit consumes 30W (max)

ENVIRONMENTAL

- Qualification : pre thermal vibration-thermal cycling & post thermal vibration test
- Temperature range : -30°C and +70°C (Storage)
-10°C and +55°C (Operational)

PART NUMBER(S)

CT1030	ATE for Array Interface Board
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